

IN THE CLAIMS:

The following listing of claims will replace all prior versions and listings of claims in the application.

- 1 1. (currently amended) A method for improving performance of an audio chip
- 2 including a DSP[,] comprising ~~the steps of~~:
- 3 providing an apparatus having a plurality of elements running in parallel
- 4 with ~~said~~ the DSP;
- 5 configuring ~~said~~ the apparatus to perform a function according to a
- 6 configuration setup; and
- 7 employing ~~said~~ the apparatus for accessing data from ~~said~~ the plurality of
- 8 elements in a pipeline structure to maximize utilization of ~~said~~ the plurality of
- 9 elements, wherein data is accessed in a four-cycle pipeline structure in a pre-
- 10 multiplication function, in an IFFT function, and in a post-multiplication function;
- 11 wherein data is accessed in a six-cycle pipeline structure in a biquad mode; and
- 12 wherein data is accessed in a nine-cycle pipeline structure in a double precision
- 13 biquad mode.

- 1 2-18. (Cancelled)